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## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10044777	FILING DATE 01/11/2002	CLASS 257	SUBCLASS 283	GA. J. 283	EXAMINER NO. 41
<b>**APPLICANTS:</b> Chason Marc; Darvir Janice; Qi Jing; Yala Nadia;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			CMO15331(72804)		
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor package device and method					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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